

**IN THE CLAIMS**

1 -60. (Canceled)

61. (Previously Presented) A method for accessing several different locations in an asynchronously-accessible memory device, comprising:

selecting a pipeline mode of operation;

providing an initial external address associated with asynchronously accessing the asynchronously-accessible memory device in the pipeline mode of operation;

switching modes to a burst mode of operation;

while in the burst mode of operation, generating at least one subsequent internal address patterned after the initial external address provided while in the pipelined mode of operation; and

providing a new external address for every access associated with asynchronously accessing the asynchronously-accessible memory device while in the burst mode of operation.

62. - 64. (Canceled)